

# REISSUE PATENT APPLICATION TRANSMITTAL

Address to:

Assistant Commissioner for Patents  
 Box Patent Application  
 Washington, DC 20231

Attorney Docket No.

693-009

First Named Inventor

Smallwood et al

Original Patent Number

5,654,609

Original Patent Issue Date  
(Month/Day/Year)

08/05/97

Express Mail Label No.

EL 425384421US

## APPLICATION FOR REISSUE OF:

(check applicable box)



Utility Patent



Design Patent



Plant Patent

## APPLICATION ELEMENTS

1. ☒ \* Fee Transmittal Form (PTO/SB/56)  
(Submit an original, and a duplicate for fee processing)
2. ☒ Specification and Claims (amended, if appropriate)
3. ☒ Drawing(s) (proposed amendments, if appropriate)
4. ☒ Reissue Oath / Declaration (original or copy) (2)  
(37 C.F.R. § 1.175)(PTO/SB/51 or 52)
5. Original U.S. Patent  
☒ Offer to Surrender Original Patent (37 C.F.R. § 1.178)  
 (PTO/SB/53 or PTO/SB/54) (2)  
 or  
☐ Ribboned Original Patent Grant  
☐ Affidavit / Declaration of Loss (PTO/SB/55)
6. Original U.S. Patent currently assigned?  
☒ Yes ☐ No  
 (If Yes, check applicable box(es))

## ACCOMPANYING APPLICATION PARTS

7. ☐ Foreign Priority Claim (35 U.S.C. 119)  
(if applicable)
8. ☒ Information Disclosure Statement (IDS)/PTO-1449 ☒ Copies of IDS Citations
9. ☐ English Translation of Reissue Oath/Declaration  
(if applicable)
10. ☐ \* Small Entity Statement(s) ☐ Statement filed in prior application, Status still proper and desired (PTO/SB/09-12)
11. ☒ Preliminary Amendment
12. ☒ Return Receipt Postcard (MPEP 503)  
(Should be specifically itemized)
13. ☒ Other: Check for \$1060.00

\* NOTE FOR ITEMS 1 & 10: IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27), EXCEPT IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).

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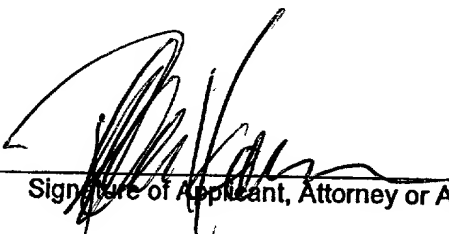
Signature

Date

8/4/99

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REISSUE APPLICATION FEE TRANSMITTAL FORM						Docket Number (Optional)		
<b>Claims as Filed - Part 1</b>								
Claims in Patent	For	Number Filed in Reissue Application	(3) Number Extra	Small Entity		Other than a Small Entity		
				Rate	Fee	Rate	Fee	
(A) 6	Total Claims (37 CFR 1.16(i))	(B) 28	**** 8 =	x \$	=	or	x \$ 18 = 144	
(C) 3	Independent Claims (37 CFR 1.16(i))	(D) 5	* 2 =	x \$	=		x \$ 78 = 156	
Basic Fee (37 CFR 1.16(h))				\$			\$ 760	
Total Filing Fee				\$		OR	\$ 1,060	
<b>Claims as Amended - Part 2</b>								
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Extra Claims Present	Small Entity		Other than a Small Entity	
					Rate	Fee	Rate	Fee
Total Claims (37 CFR 1.16(i))	***	MINUS	**	=	x \$	=	or	x \$ =
Independent Claims (37 CFR 1.16(i))	***	MINUS	*****	=	x \$	=		x \$ =
Total Additional Fee				\$		OR	\$	
<p>* If the entry in (D) is less than the entry in (C), write "0" in column 3.</p> <p>** If the "Highest Number of Total Claims Previously Paid For" is less than 20, Write "20" in this space.</p> <p>*** After any cancellation of claims</p> <p>**** If "A" is greater than 20, use (B - A); if "A" is 20 or less, use (B - 20).</p> <p>***** "Highest Number of Independent Claims Previously Paid For" or Number of Independent Claims in Patent (C).</p>								
<p><input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____.</p> <p>A duplicate copy of this sheet is enclosed.</p>								
<p><input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees under 37 CFR 1.16 or 1.17 which may be required, or credit any overpayment to Deposit Account No. <u>19-2825</u>.</p> <p>A duplicate copy of this sheet is enclosed.</p>								
<p><input checked="" type="checkbox"/> A check in the amount of \$ <u>1060.00</u> to cover the filing / additional fee is enclosed.</p>								
<u>8/4/99</u> Date		 Signature of Applicant, Attorney or Agent of Record <u>Robert M. Haroun</u> Typed or printed name <u>Reg. No. 34,345</u>						

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Reissue Patent Application of  
Smallwood et al. :  
U.S. Patent No. 5,654,609 :  
Patent Granted: August 5, 1997 :  
Title: GAS DISCHARGE LAMP AND POWER :  
DISTRIBUTION SYSTEM THEREFOR :  
-----X

CONSENT OF ASSIGNEE TO FILE REISSUE APPLICATION UNDER 37 CFR 1.172

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

S I R:

This Consent of Assignee is submitted pursuant to 37 CFR 1.172. Assignee,  
LOGIC LABS, INC., hereby consents to the filing of a reissue patent application in  
connection with the above-referenced U.S. patent so as to broaden the claims thereof. The  
undersigned has the authority to act on behalf of the assignee, LOGIC LABS, INC., pertaining  
to the reissue application at issue.

Signature:

*Robert J. Smallwood*

Name:

*Robert J. Smallwood*

Title:

*Vice President R&D*

Date:

*8-2-99*

Attorney Docket No. 693-009

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In re Reissue Patent Application of  
Smallwood et al. :

U.S. Patent No. 5,654,609:

Patent Granted: August 5, 1997 :

Title: GAS DISCHARGE LAMP AND POWER :  
DISTRIBUTION SYSTEM THEREFOR :  
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**CERTIFICATE UNDER 37 CFR 3.73(b)**  
**ACCOMPANYING REISSUE APPLICATION**

**Logic Laboratories, Inc.**, a corporation, certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of either:

**A. An assignment from the inventor(s) of the patent application identified above. The assignment was recorded in the Patent and Trademark office at Reel Frame or for which a copy thereof is attached.**

**OR**

**B. A chain of title from the inventor(s), of the patent application identified above, to the current assignee as shown below:**

1. From: **Michael P. Zarich** To: **American Compact Lighting, LLC**  
The document was recorded in the Patent and Trademark Office at  
Reel **7358** , Frame **0593** , or for which a copy thereof is attached.
2. From: **American Compact Lighting, LLC** To: **Logic Laboratories, Inc.**  
The document was recorded in the Patent and Trademark Office at  
Reel **7927** , Frame **0139** , or for which a copy thereof is attached.
3. From: **Logic Laboratories, Inc.** To: **Robert C. Smallwood**  
The document was recorded in the Patent and Trademark Office at

Reel 7919 , Frame 0836 , or for which a copy thereof is attached.

4. From: Robert C. Smallwood To: Logic Laboratories, Inc.  
The document was recorded in the Patent and Trademark Office at  
Reel 8104 , Frame 0123 , or for which a copy thereof is attached.


Copies of assignments or other documents in the chain of title are attached.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: 8-2-89

  
Robert Smallwood  
Vice President, R&D  
Logic Laboratories, Inc.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Reissue Patent Application of  
Smallwood et al. :  
U.S. Patent No. 5,654,609 :  
Patent Granted: August 5, 1997 :  
Title: GAS DISCHARGE LAMP AND POWER :  
DISTRIBUTION SYSTEM THEREFOR :  
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**PRELIMINARY AMENDMENT**

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

S I R:

Prior to the examination of the above-identified reissue application, please enter  
the following amendments and remarks.

**In the Claims:**

Please amend the existing patent claims as follows:

1. (Amended) A module for driving a gas discharge lamp having heater elements  
contained within an envelope in response to electrical power from a source comprising:  
means for receiving power from the source;  
an oscillator coupled to said receiving means for transforming said received  
power to an output signal at a frequency and voltage for causing the lamp to produce visible light

through gas discharge within the lamp envelope;

[an ]a circuit board mounting said oscillator within a volume having a cross-section configured substantially the same as the cross-section of the lamp envelope; and

means attaching said board for forming an end of the lamp envelope with said oscillator output signal connected to the lamp heater elements.

5. (Amended) A module for driving a gas discharge lamp having heater elements contained within an envelope in response to electrical power from a source comprising:

means for receiving power from said source;

an oscillator coupled to said receiving means for transforming said power to an output signal at a frequency and voltage for causing said lamp to produce visible light through gas discharge within said [aid] lamp envelope;

a circuit board mounting said oscillator within a volume having a cross-section which is substantially the same as a cross-section of said lamp envelope; and

means for attaching said circuit board so that said oscillator output signal is connected to said lamp heater elements.

Please add the following new claims:

--7. A module for driving a gas discharge lamp in response to electrical power from a source, said module comprising:

means for receiving power from the source;

an oscillator coupled to said receiving means for transforming said received

power to an output signal at a frequency and voltage for causing the lamp to produce visible light through gas discharge within said lamp; and

a circuit board having said oscillator mounted thereon, wherein said circuit board is attached to said lamp.

8. A module in accordance with claim 7, wherein said circuit board is attached externally to said lamp.

9. A module in accordance with claim 7, wherein said circuit board is attached internally to said lamp.

10. A module in accordance with claim 7, wherein said lamp comprises an envelope.

11. A module in accordance with claim 10, wherein said envelope contains heater elements.

12. A module in accordance with claim 11, wherein said oscillator output signal is coupled to said heater elements.

13. A module in accordance with claim 12, said module further comprising means for attaching said oscillator to said heater elements.



14. A module in accordance with claim 13, wherein said attaching means comprises:

an output transformer having an array of pins and a plurality of leads connecting to said heater elements; and

a receptacle mounted on said circuit board for receiving said array of pins of said output transformer.

15. A module in accordance with claim 7, wherein the power source produces standard A.C. power, said module further including means mounted on said circuit board for converting said received power for actuating said oscillator.

16. A module in accordance with claim 7, wherein the power source produces D.C. power, said module further including means for actuating said oscillator from said received power from the D.C. source.

17. A module in accordance with claim 10, wherein said circuit board mounts said oscillator within a volume having a cross-section which is substantially the same as a cross-section of said lamp envelope.

18. A module for driving a gas discharge lamp in response to electrical power from a source, said module comprising:

means for receiving power from the source;

an oscillator coupled to said receiving means for transforming said received power to an output signal at a frequency and voltage for causing the lamp to produce visible light through gas discharge within said lamp; and

a circuit board having said oscillator mounted thereon, wherein said circuit board is contained in a housing, said housing attached to said lamp.

19. A module in accordance with claim 18, wherein said housing containing said circuit board is attached externally to said lamp.

20. A module in accordance with claim 18, wherein said housing containing said circuit board is attached internally to said lamp.

21. A module in accordance with claim 18, wherein said lamp comprises an envelope.

22. A module in accordance with claim 21, wherein said housing has a cross-section which is substantially the same as a cross-section of said lamp envelope.

23. A module in accordance with claim 21, wherein said envelope contains heater elements.

24. A module in accordance with claim 23, wherein said oscillator output

signal is coupled to said heater elements.

25. A module in accordance with claim 24, said module further comprising means for attaching said oscillator to said heater elements.

26. A module in accordance with claim 25, wherein said attaching means comprises:

an output transformer having an array of pins and a plurality of leads connecting to said heater elements; and

a receptacle mounted on said circuit board for receiving said array of pins of said output transformer.

27. A module in accordance with claim 18, wherein the power source produces standard A.C. power, said module further including means mounted on said circuit board for converting said received power for actuating said oscillator.

28. A module in accordance with claim 18, wherein the power source produces D.C. power, said module further including means for actuating said oscillator from said received power from the D.C. source.--

## **Remarks**

### **I. Introduction**

Original U.S. Patent No. 5,654,609 has claims 1-6.

This reissue application hereby adds new claims 7-28.

Broadened claims 7-28 have been added because the original patent upon which this reissue application is based is partly inoperative or invalid by reason of patentee claiming less than patentee had the right to claim.

No new matter has been added.

Thus, claims 1-28 are pending in this reissue application and are hereby presented for consideration by the Examiner.

### **II. Statement in Support of Amendments**

In accordance with 37 CFR 1.121(b)(2)(iii), Applicant hereby submits the following explanation of the support in the disclosure of the patent for the above-referenced amendments.

The amendment of claims 1 and 5 are made to correct typographical errors in the text of U.S. Patent No. 5,654,609.

New independent claim 7 relates to a module for driving a gas discharge lamp in response to electrical power from a source. The module comprises a means for receiving power from the source. An oscillator is coupled to the receiving means for transforming the received

power to an output signal at a frequency and voltage for causing the lamp to produce visible light through gas discharge within the lamp envelope. A circuit board has the oscillator mounted thereon, wherein the circuit board is attached to the lamp. All of the features of this claim are supported by the original disclosure of U.S. Patent No. 5,654,609 - for instance, at col. 2, lines 43-56, and in Figures 5 and 7. In addition, the feature of the claim reciting "said circuit board is attached to said lamp" is supported by the original disclosure of U.S. Patent No. 5,654,609 which states, for instance, that "the board is attached for forming the end of the lamp ..." (col. 2, lines 54-55).

Claims 8-17, which depend from independent claim 7, are also supported by the disclosure of the original patent. For instance, claim 8 adds to claim 7 the limitation that the circuit board is attached externally to the lamp. This limitation is supported by the original disclosure of U.S. Patent No. 5,654,609, which states, for instance, that "[i]t is possible to attach the circuit board externally to the end of the lamp envelope" (col. 2, lines 59-61).

Claim 9 adds to claim 7 the limitation that the circuit board is attached internally to an end of the lamp envelope. This limitation is supported by the original disclosure of U.S. Patent No. 5,654,609, which states, for instance, that "the circuit board [is] positioned within the lamp envelope" (col. 2, lines 57-58).

Claim 10 adds to claim 7 the limitation that the lamp comprises an envelope. Claim 11 adds to claim 10 the limitation that the envelope contains heater elements, while claim

12 adds to claim 11 the limitation that the oscillator output signal is coupled to the heater elements. Each of these limitations are supported by the original disclosure of U.S. Patent No. 5,654,609, which states, for instance, that “[t]he present invention ... [has] ... a module for driving a gas discharge lamp having heater elements contained within an envelope” (col. 2, lines 57-58) and “the oscillator output signal [is] connected to the lamp heater elements” (col. 2, lines 55-56).

Claim 13 adds to claim 12 the limitation that the module further comprises means for attaching the oscillator to the lamp heater elements. This limitation is supported by the original disclosure of U.S. Patent No. 5,654,609, which states, for instance, that “[a] group of four leads extend ... to provide the connections to the heater elements” (col. 8, lines 23-25). Claim 14 further provides that the attaching means comprise an output transformer having an array of pins and a plurality of leads connecting to the heater elements and a receptacle mounted on the circuit board for receiving the array of pins of the output transformer. This additional limitation is further provided in the original disclosure of U.S. Patent No. 5,654,609 at, for instance, col. 8, lines 20-30.

Claim 15 adds to claim 7 the limitation that the power source produces standard A.C. power, and the module further including means mounted on the circuit board for converting the received power for actuating the oscillator. This limitation is supported by the original disclosure of U.S. Patent No. 5,654,609, which states, for instance, that “the power source can produce standard A.C. power with the module further including a circuit mounted on the circuit

board for converting the received A.C. power for actuating the oscillator” (col. 2, lines 62-65).

Claim 16 adds to claim 7 the limitation that the power source produces D.C. power, and the module further including means for actuating the oscillator from said received power from the D.C. source. This limitation is also supported by the original disclosure of U.S. Patent No. 5,654,609, which states, for instance, that “[i]f the power source produces D.C. power, the module includes means for actuating the oscillator from said received power from the D.C. source” (col. 2, lines 65-67).

Claim 17 adds to claim 10 the limitation that the circuit board mounts the oscillator within a volume having a cross-section which is substantially the same as a cross-section of the lamp envelope. This limitation is supported by the original disclosure of U.S. Patent No. 5,654,609, which states, for instance, that “[the] circuit board mounts the oscillator within a volume having a cross-section configured substantially the same as the cross-section of the lamp envelope ” (col. 2, lines 52-54).

New independent claim 18 relates to a module for driving a gas discharge lamp in response to electrical power from a source. The module comprises a means for receiving power from the source. An oscillator is coupled to the receiving means for transforming the received power to an output signal at a frequency and voltage for causing the lamp to produce visible light through gas discharge within the lamp envelope. A circuit board has the oscillator mounted thereon. The circuit board is contained in a housing, and the housing is attached to the lamp. All

of the features of this claim are supported by the original disclosure of U.S. Patent No. 5,654,609 - for instance, at col. 2, lines 43-56, and in Figures 5 and 7. In addition, the feature of the claim reciting "said circuit board is contained in a housing, said housing attached to said lamp" is supported by the original disclosure of U.S. Patent No. 5,654,609 which shows a cylindrical unit 110 in Figure 5 and which states, for instance, "the module including a sleeve for retaining the board within" (col. 2, lines 60-62). Claims 19-28, which depend from claim 18, are also supported by the disclosure of the original patent, as discussed above in connection with claims 8-17, respectively.

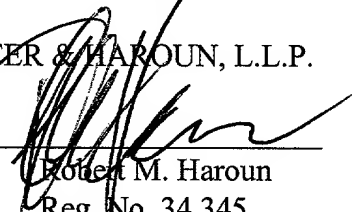
### **Conclusion**

In view of the aforementioned preliminary amendment and remarks, it is respectfully submitted that all claims currently pending in the above identified reissue application are now in condition for allowance, the earliest possible notice of which is earnestly solicited. If in the Examiner's opinion the prosecution of the present reissue application would be advanced by a telephone interview, he is invited to contact the undersigned at the telephone number listed below.

Respectfully submitted,

SOEFER & HAROUN, L.L.P.

By

  
Robert M. Haroun  
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Suite 1921  
New York, NY  
(212) 697-2800

Dated: August 4, 1999



## GAS DISCHARGE LAMP AND POWER DISTRIBUTION SYSTEM THEREFOR

This is a continuation of application Ser. No. 08/116,150 filed on Sep. 2, 1993 now patented U.S. Pat. No. 5,485,057.

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

The present invention relates to gas discharge lamp apparatus and methods as well as to power distribution systems and processes useful in association with such lamps although the power distribution feature is not necessarily limited to the discharge lamp application. More particularly, the present invention relates to fluorescent lamps, mercury vapor lamps, sodium or metal halide lamps, as well as to other electronic loads. This invention is especially useful for lighting systems and power distribution associated with such lighting systems.

#### 2. Description of the Prior Art

Contemporary lighting systems distribute power, such as at 60 Hertz, 115 volts RMS (or 220 volts RMS), to a variety of fixtures containing gas discharge lamps. Within each fixture is a solid state ballast which functions as an electronic controller to filter and convert the A.C. power to direct current. The D.C. is next converted to a sinusoidal source, such as 20 kilohertz, to provide operating power to the lamps. Examples of prior art ballast circuits for fluorescent lamps are shown in U.S. Pat. Nos. 3,753,071 by Engel et al; 4,109,307 by Knoll; and 4,259,614 by Kohler, as well in the Patent Cooperation Treaty Publication WO 91/16802 by Smallwood et al.

Distributed power systems for fluorescent lamps are known in the prior art, such as in U.S. Pat. Nos. 4,508,996 by Clegg et al and 5,047,696 by Nilssen. Clegg et al show D.C. driver inverters for producing high frequency signals for driving clusters of lamps. Nilssen converts primary line power to D.C., and then to 30 kHz in an inverter for parallel distribution to a plurality of light units. Power is coupled through a passive auto-transformer network to drive fluorescent light bulbs in pairs. The Clegg et al patent employs resonant circuits.

The prior art power distribution system for use in mines includes power units employing a signal at 30 kHz to drive a plurality of lamps, such as in British Patent 1,401,628. U.S. Pat. No. 4,293,799 by Roberts column 1, lines 32-42, describes the prior art to their patent as including a system wherein a plurality of "intrinsic safety" mine lamps are powered by a common power unit in a manner suggested by the aforementioned British patent. The Roberts patent shows a master unit driving a plurality of parallel connected slave units, and alleges its contribution is an improvement to such a system in the form of voltage control elements in-line between the master oscillator and the slave type units to reduce the prospect of sparking. That is, the Roberts patent includes a voltage controller between the power source and the series circuit, including the primary winding of a transformer for limiting the amount of power to a level below that which is "incendive to the atmosphere" for mine safety.

Various prior art has addressed modification of lamp envelopes to include different components. For instance, U.S. Pat. No. 4,571,526 by Wesselink shows a mercury vapor lamp configured with the discharge elements surrounding the ballast as a sealed unit. A special heat conductive thin-walled member is included for the ballast. U.S. Pat. No. 3,549,941 by Friedmann shows fluorescent lamps having starter elements, a relay and operational elements all within the lamp envelope.

U.S. Pat. No. 4,316,121 by Hammer et al also shows a lamp ballast and fluorescent bulb packaging unit. It utilizes an inductive-resistive element formed as an elongated coil parallel to the lamp tube. U.S. Pat. No. 4,857,806 by Nilssen is another form of folded lamp with ballasting circuitry in the base so it can accommodate screwing into a standard socket.

Each ballast must ensure low electromagnetic radiation, must reduce conducted noise reflected into the main power line from its internal electronics, and ideally should present as near as possible a unity power factor to the main A.C. power mains.

The contemporary fluorescent lamp ballasts typically contain rectifiers, capacitors, transistors, integrated circuits and transformers to accomplish the power conversion function. Each ballast may contain in excess of forty individual components. While a single ballast may power from one to four or more gas discharge lamps at one time, for large distributed lighting requirements in factories and department stores, for instance, thousands of these controllers are required.

### SUMMARY OF THE INVENTION

Traditional systems require a solid state ballast to both condition the input A.C. power and to provide high frequency sinusoidal power to the lamps. The input A.C. power conditioning includes surge protection, filtering and more recently active input power factor correction circuitry. The output or lamp drive circuitry contains a high frequency oscillator and transformer. The traditional ballast is a large unit (i.e., about twenty-five cubic inches) which requires a certified electrician to install.

One of the features of the present invention is a high frequency oscillator circuit miniaturized for containment within the envelope of a lamp, or as a small module attached to the end of the lamp. The power oscillator volume is a cylinder of less than five cubic inches. Sufficient circuitry is provided such that the lamp will function either as a stand-alone unit connected to primary A.C. power, or as a slave unit connected to a power factor correction unit as a master in large, multiple fixture installations.

The present invention is concerned with the apparatus and method of construction of a module for driving a gas discharge lamp having heater elements contained within an envelope and for doing this in response to electrical power from a source. Power is received from that source with an oscillator coupled to the received power for transforming that received power to an output signal at a frequency and voltage suitable for causing the lamp to produce visible light through gas discharge within the lamp envelope. An elongated circuit board mounts the oscillator within a volume having a cross-section configured substantially the same as the cross-section of the lamp envelope. The board is attached for forming an end of the lamp envelope with the oscillator output signal connected to the lamp heater elements.

With the circuit board positioned within the lamp envelope, the power is receivable via prong-type conductor pins extending from an endcap. It is possible to attach the circuit board externally to an end of the lamp envelope with the module including a sleeve for retaining the board there-within. The power source can produce standard A.C. power with the module further including a circuit mounted on the circuit board for converting the received A.C. power for actuating the oscillator. If the power source produces D.C. power, the module includes means for actuating the oscillator from the received power from the D.C. source.

Typically, fluorescent lamp envelopes contain first and second heater elements at respective ends of the envelope. A module, in accordance with this invention, includes a direct connection of heater power from the oscillator output to the first of the heater elements. The module further includes conductors extending the length of the envelope to the second of the heater elements for connecting the oscillator output thereto. These conductors are preferably positioned along the inner surface of the envelope to minimize damage in handling.

The power factor correction unit employed in the present invention is designed to accept conventional 50/60 Hz A.C. power to filter and power factor correct this energy, and to provide smooth D.C. voltage, such as 110 volts. The power factor correction unit may supply from one to tens of fluorescent ballast lamps in parallel. In small lamp installations, such as for workshop or home use, the ballast lamp will function directly from the main A.C. power lines without the necessity of an intervening power factor correction unit.

Additionally, in large, multiple fixture installations, a qualified electrician is not required to install numerous individual ballasts. The wiring is simplified to attaching A.C. power only to the fixture as the ballast is wholly contained with the lamp.

The simplicity of the slave ballast units permits configuring those slaves so they will fit as an attachment module on the end of a fluorescent lamp. Alternatively, manufacture of the lamps can include the slave ballast as an integral part thereof.

A conventional gas discharge lamp is designed to include a miniature electronic power oscillator which will both heat the cathode and supply the necessary high voltage and current to illuminate the fluorescent type of gas discharge tube. The electronics is constructed into a module which fits completely inside the envelope of the gas discharge lamp, or is attachable as a module to the end of the lamp envelope. At one end of the tube, electrodes are provided to connect a D.C. source of energy, such as 110 volts or conventional 110 volts AC, 60 Hz, which supplies power to the internal electronic oscillator. The electronics and associated electrodes are constructed in one integrated assembly which aides in the manufacture, test, and assembly of the lamp.

Additionally, the unit is so constructed as to optimize the heat removal from the circuitry. The power oscillator internal to the tube produces sine wave power at several hundred kilohertz which aides in reducing the size of the internal magnetic components as well as in minimizing the electromagnetic field emissions from the lamp. The miniature power oscillator is placed in a ferrous metal cylinder to facilitate heat transfer and reduce electromagnetic radiation.

The direct current (110 volts D.C., for example) potential required to operate the ballast lamp is designed with a magnitude which is easily derived from conventional existing 50 Hz or 60 Hz power sources. The size and power level of the D.C. source is selected so as to enable powering from one ballast lamp to tens of them in parallel. The ballast lamp thus makes possible fluorescent lighting systems with centralized power conditioning equipment. Such new systems eliminate bulky and redundant ballasts which are presently required to operate fluorescent lighting systems. The two-pronged connector at one end of the lamp facilitates installing or changing lamps which are mountable, either vertically or horizontally, while retaining the relative stability of contemporary fixtures for receiving elongated fluorescent lamps.

Another feature of the present invention relates to placement within the gas discharge lamp envelope a complete, highly efficient power oscillator circuit, thereby eliminating bulky components which are presently mounted external to the lamp. A single electronic assembly is provided through the use of integrated circuits and surface mount technology which will enable manufacture of the gas discharge lamp in accordance with this invention in a simple, reliable process. The invention permits design of a unit for D.C. input levels which are readily obtainable from existing conventional A.C. power sources, thereby adding to the universality of the lamp.

A second feature of the present invention relates to apparatus and processes for separating the power conditioning elements associated with a fluorescent lighting system in a common master ballast unit which parallel feeds a plurality of slave ballast units either attached to, or embedded in, the envelope of an elongated fluorescent type lamp. While the invention is primarily intended for fluorescent systems, it is believed it may have wider power distribution significance.

Accordingly, the present invention is directed to a gas discharge lamp power distribution system for fluorescent lamps, or the like, and is composed of a master unit and one or more gas discharge lamps adapted to cooperate with that master unit. The master unit has an input and an output with the input receiving A.C. power from a source. Power factor correction is performed in the master unit, and reflected to the source at the master unit input. The A.C. power is converted to D.C. power at the master unit output.

It is thus possible to parallel couple a plurality of gas discharge lamps to the master unit output. Each of those lamps is configured with an elongated envelope having a light producing medium contained therein. A module is associated with the envelope to interface with the master unit output. The module includes a circuit board having an oscillator circuit mounted thereon, with that board positioned at one end of the elongated envelope. Further, the board assembly is constructed to have a cross-section conforming as an extension of the elongated envelope. The output of the oscillator circuit is applied for exciting the medium, as by energizing heater elements and placing a potential across the medium to cause it to produce light.

Those having normal skill in the art will recognize the foregoing and other objects, features, advantages and applications of the present invention from the following more detailed description of the preferred embodiments as illustrated in the accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic diagram of a typical prior art power distribution system for fluorescent lamps.

FIG. 2A is a fluorescent lamp power distribution system in accordance with the present invention with ballast lamps coupled in parallel directly to an A.C. main.

FIG. 2B is a fluorescent lamp power distribution system in accordance with the present invention powered by a converter and power factor correction master unit.

FIG. 3 is a general block diagram of the master conditioning unit of the FIG. 2B embodiment.

FIG. 4 is a somewhat idealized schematic diagram of a gas discharge lamp including the power oscillator as an integral element thereof.

FIG. 5 is an alternate embodiment of an attachment of the ballast module to a lamp envelope.

FIG. 6 is a circuit diagram of a power oscillator suitable for use with the ballast lamp structure of the present invention.

FIG. 7 is an isometric view of an electronics assembly constructed for placement within a lamp envelope or for incorporation in a module attachable to the end of the lamp envelope.

FIG. 8 is a partially-sectioned view of a gas discharge lamp with the envelope thereof formed in a U-shaped configuration with the control elements contained within that envelope.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

A conventional fluorescent lamp power distribution system is shown in schematic diagram form in FIG. 1 wherein standard input A.C. power 10 is introduced at the input terminals. In the United States, this input is usually 60 Hz at 110 or 220 volts A.C. This power is introduced in parallel to a plurality of assemblies associated with each of an array of lamp fixtures. These assemblies include a converter, or ballast unit 12A-12N which converts the primary A.C. input to a high frequency, such as 20-30 kilohertz, to drive a set of typically two series connected fluorescent lamps. Thus, ballast unit 12A drives lamps 14A and 15A in series.

Lamp power distribution systems in accordance with this invention are shown in FIGS. 2A and 2B wherein the primary commercially-available power 10 is coupled to either system. A stand-alone configuration is shown in FIG. 2A wherein the main power 10 is connected in parallel into a plurality of lamps 11A-11N each of which is self-sufficient in that it contains its own power handling elements preferably including a conducted line noise filter and a power oscillator along the lines of those described later herein.

FIG. 2B is likewise driven by a conventional power system at input 10, but includes at least one master power converting unit 20 the output 22 of which is coupled in parallel into a plurality of slave ballast lamps 24A-24N. This reduces the amount of circuitry that each lamp 24 must incorporate thereby decreasing the volume of the module that is implanted in, or attached to, the lamp envelope while increasing its efficiency and heat handling capabilities. Conventional power factor correction circuits require acceptance of rather large components (frequently bulky capacitors) for bulk energy storage and filtering thereby increasing the volume of the module at the lamp should that module include such circuitry. Thus, inclusion of the power factor correction in the master unit 20 relieves the lamps of that volumetric burden associated with that function. Master ballast units 20 contain input filters, preferably along with suitable power factor correction circuits, to convert the 60 Hz power internally to a high voltage D.C. while presenting better than 0.99 power factor loading to the A.C. source.

The major sections of the master ballast 20 are shown in sequential block form in FIG. 3. The 110/220 VAC input 10 is initially passed through surge protection unit 26 where it is also filtered. The active power factor of the filtered output from block 26 is corrected initially in block section 27 via conventional control circuitry. The A.C. is next rectified in block 27 which can include other electronic functions, if desired, such as fault detection. Output power switching is accomplished by a network of output diodes and filters 29 which finally produces the D.C. desired for output bus 22. Thus the power conditioning functionality and complexity of a traditional ballast is concentrated in one, much higher powered unit 20. Typically, master ballast 20 is capable of driving a bank of thirty or more ballast lamps where those lamps include their own oscillator circuit and gas discharge operating components, such as described later herein, for instance.

In operation, master ballast 20 (shown in general block form in FIG. 3) receives the input power from a conventional power main 10. The primary input power 10 is initially passed through circuitry 26 for surge protection and common mode and differential filtering. The A.C. is then rectified and appropriate power factor correction reflected back into the primary mains 10 via circuit 27 which likewise drives the control electronics. Output diodes and filters apply the resulting output from circuit block 29 into secondary lamp feed bus 22.

A block diagram of an embodiment of a ballast lamp 100 is shown in FIG. 4. The key to utilizing existing fluorescent lamp design with its 20,000 plus hours of life expectancy is to provide adequate heating power to the cathodes 101 and 102. For ballast lamp 100, this is accomplished by routing two thin wires 104 and 105 down the lamp wall to the end heater 102. The potential difference between these wires is developed by the power oscillator 108 in assembly 106 and its associated output transformer 109. After lamp ignition, as in conventional ballasts, this potential is approximately 2.5 volts RMS. The power oscillator 108 similarly heats the local heater or cathode 101. The high voltage starting and running potential are established as in a conventional ballast across the secondary of the power oscillator output transformer 109. The potential difference between local heater 101 and end heater 102 is thus 110 VRMs during normal lamp operation. This potential is, however, in the form of a high frequency sine wave with a nearly perfect crest factor (1.414).

As shown in FIG. 5, the power oscillator assembly can take the form of a cylindrical unit 110, containing the hybrid electronics and miniaturized magnetics. Four leads 111, 112, 113 and 114, which are internal to the envelope of tube 115, are connected to respective output pairs 116 and 118 of module 110, and the entire power oscillator assembly is preferably bonded to tube 115 somewhat like the manner a two-pronged endcap is attached to contemporary fluorescent lamps.

The lamp shown in FIG. 5 is intended for a system wherein the A.C. main power is converted to D.C. power at a remote unit (such as master ballast 20 of FIG. 2B) which is then connected to the lamp. Thus, the two different shaped electrodes 120 and 121 are respectively cylindrical and rectangular, and serve to key the lamp during installation so that the correct polarity of the high voltage D.C. is applied. A rectangular cross-section is acceptable for cylindrical terminal 120, although preferably at 90° to ground terminal 121 to provide an alternate polarity protection scheme. The internal electronic oscillator also contains reverse polarity protection to prevent tube damage for improperly-wired ballast lamp sockets.

Note that keying of the connection electrodes is not necessary if the module 110 employs a full wave rectifier, such as a diode bridge which can handle any polarity, and protects against polarity reversal. It is acceptable to employ pins similar to a contemporary fluorescent lamp for ease of installation, but preferably with a different pin orientation such as wider or narrower spacings between the pins. This would prevent inadvertent installation of the lamp in a conventional fixture.

The outside shell of the oscillator assembly 110 is constructed of a thin sheet of ferrous metal. The internal electronics are mounted to a metal substrate which is attached to the outer shell. Thus, a heat conduction path is established to help minimize the internal electronic temperatures. The metal also serves as an electromagnetic shield to

minimize radiation. The shell may further be attached to the metal structure of lamp fixtures to further increase the heat sink capability of the ballast lamp.

FIG. 6 is a schematic diagram of the power oscillator which is a miniaturized electronic ballast configured to fit inside the fluorescent tube. Most (ninety-five percent or more) of the components are surface mount devices. The exceptions are components, such as the transformer, including primary 130 and secondaries shown at 134 and 135, bulk storage capacitors 131 and 132, inductive coupler 133, and several diodes. Circuit board pins 136 and 137 are connected to the two prongs at one endcap of the fluorescent tube. A.C. power 10, typically 90 to 130 volts RMS, 50 or 60 Hz, is applied to the tube.

The far end heater of the tube is connected via wires passing the length of the tube. While external placement of the end heater wires is acceptable, they are preferably inside the tube and coupled to circuit board pins 143 and 144 of the output transformer secondary 135. The near end heater of the tube is connected to pins 145 and 146. The tube socket containing pins 136 and 137 and the power oscillator circuit are one integrated part that is mated with the tube wires at the time of tube assembly either permanently or detachably.

In operation, the FIG. 6 power oscillator functions as follows. Transient voltage suppressor (TVS) 140 prevents spikes and surges from damaging the electronics. Capacitor 141 and inductive coupler 133, along with capacitor 142, form a common mode EMI filter. Diode bridge 148 is an integrated circuit diode bridge for rectifying the input A.C. power. Resistor 151 and Zener diode 150 are a bootstrap circuit (typically 15 volt) to supply initial power to integrated circuit chip 156. Resistors 152 and Zener diode 153 likewise provide +5 volt bootstrap power for integrated circuits 156, 157 and 158. Capacitors 154 and 155 are bulk storage capacitors for these DC supplies.

Secondary winding 134, diodes 161 and 162, and resistor 163 feed auxiliary power to the +5 and +15 volt supplies after the power oscillator has started. Integrated circuit 157 is a CMOS 555 timer chip which provides several hundred kilohertz clock signals to integrated circuit chip 158. The network of resistors 164 and 165 and capacitor 166 set the oscillator frequency while capacitors 167 and 168 are noise decoupling capacitors. Clock signals from chip 157 are fed to chip 158 which is an HCT74 flip-flop coupled to divide the oscillator clock signals by two to provide a symmetrical square wave signal to integrated circuit 156. While the circuit disclosed was constructed to produce an output signal at transformer 130 with a frequency of 220 kilohertz, it is believed advantageous to design the circuit to function in the megahertz range.

Integrated circuit 156 is preferably an IR2110S gate driver circuit. Power MOS FETS 171 and 172 are connected in a half-bridge configuration to drive the primary 130 of the output transformer. Capacitor 173 is the bootstrap capacitor for chip 156, while diode 162 is the charging diode for capacitor 173. Capacitors 174 and 175 are decoupling capacitors for chip 156. Networks of resistor 176 and diode 177, as well as resistor 178 and diode 179, prevent cross-conduction of 171 and 172, thereby minimizing power loss and EMI generation. It is possible to realize cross-conduction prevention by including some AND gates between the output of the divider circuit 158 and the driver chip 156. Such gates would logically AND the short pulses from the oscillator circuit 157 with the divider 158 output to prevent cross-conduction at the output FET transistors 171 and 172 which drive the primary of output transformer 130.

Capacitor 180 is the resonant tank capacitor for the output transformer so that while square wave drives appear across primary 130, a quasi-sinusoidal drive is actually applied to the fluorescent tube. After tube ignition, windings 184 of secondary 135 applies approximately 100 volts RMS across the tube, while windings 183 and 185 apply power (such as at 2.7 volts RMS) across the far-and-near end heaters, respectively.

An exemplary combination of electronic elements adapted for use in conjunction with the envelope of a gas discharge lamp is presented in FIG. 7. The planar circuit board 186 is shown with through-hole mounted components 187-191 on the upper surface, and surface mounted components 192-195 on the lower surface of board 186. Element 187 might represent a diode bridge assembly, while 188 and 189 are resistors and 190 and 191 are bulk capacitors or inductor type components. The surface mounted elements 192-195 might include integrated circuit chips, surface mounted resistors and capacitors or the like.

Board 186 has a receptacle 198 mounted on one end as shown for receiving the pins of array 199 extending from output transformer 200. A group of four leads 201 extend from transformer 200 to provide the connections to the heater elements, such as 143-146 of FIG. 6, and the heater connections of FIG. 4 for instance. Another group of four leads 205 couple the primary of the transformer and the secondary winding which functions with the oscillator circuit, such as the FIG. 6 transformer primary 130 and secondary winding 134.

Although not shown in FIG. 7, outer end 196 of board 186 preferably would mount within an end cap having dual power connecting prongs for receiving the A.C. or D.C. input power. Note that the opposite end (such as end 107 of lamp 100 in FIG. 4) can have any of a variety of known mounting structures as desired. It could include dummy pins similar to contemporary fluorescent lamp bulbs if contemporary fluorescent receptacles elements are employed. Otherwise, a blank endcap and receptacle would suffice for retaining the bulb within a fixture.

FIG. 8 illustrates yet another embodiment of a gas discharge lamp 210 having the elongated envelope 211 constructed preferably with a tubular, or semi-circular, cross-section but in a "U" shape. Base 215 is secured to envelope 211 as shown with pins 216 and 217 adapted to plug into an A.C. receptacle to provide the primary power source. The thus received A.C. power is connected to a module composed of a board 220 and output transformer 221. Board 220 has the oscillator circuit and other components and circuitry including the power factor correction circuit mounted thereon for driving the output transformer 221.

The module, including board 220 and transformer 221, can be encapsulated in a heat transferring material so as to completely fill the end of the envelope 211 in which it is placed. Otherwise, the interior of envelope 211 is filled with a gas discharge medium for producing visible light upon excitation. Heater element 222 is connected directly to a pair of output connections of transformer 221, while leads 223 pass from transformer 222 through the wall of envelope 211 into the base 215 and thence through the wall of envelope 211 on its opposite end so as to connect with heater element 224. Note that it is possible to suppress undesired radio frequency radiation from the device by shielding, or by ferrite beads on the output leads as is conventional.

While the exemplary preferred embodiments of the present invention are described herein with particularity, those having normal skill in the art will recognize various



changes, modifications, additions and applications other than those specifically mentioned herein without departing from the spirit of this invention.

What is claimed is:

1. A module for driving a gas discharge lamp having heater elements contained within an envelope in response to electrical power from a source comprising:

means for receiving power from the source,

an oscillator coupled to said receiving means for transforming said received power to an output signal at a frequency and voltage for causing the lamp to produce visible light through gas discharge within the lamp envelope,

[an] a circuit board mounting said oscillator within a volume having a cross-section configured substantially the same as the cross-section of the lamp envelope, and means attaching said board for forming an end of the lamp envelope with said oscillator output signal connected to the lamp heater elements.

2. A module in accordance with claim 1 wherein said circuit board is attached externally to an end of the lamp envelope, said module including a sleeve retaining said board therewithin.

3. A module in accordance with claim 1 wherein the power source produces standard A.C. power, said module further including means mounted on said circuit board for converting said received power for actuating said oscillator.

4. A module in accordance with claim 1 wherein the power source produces D.C. power, said module further including means for actuating said oscillator from said received power from the D.C. source.

5. A module for driving a gas discharge lamp having heater elements contained within an envelope in response to electrical power from a source comprising:

- means for receiving power from said source,
- an oscillator coupled to said receiving means for transforming said power to an output signal at a frequency and voltage for causing said lamp to produce visible light through gas discharge within ~~said~~ lamp envelope.

said

- a circuit board mounting said oscillator within a volume having a cross-section which is substantially the same as a cross-section of said lamp envelope, and

- means for attaching said circuit board so that said oscillator output signal is connected to said lamp heater elements.

6. A module for driving a gas discharge lamp having heater elements contained within an envelope in response to electrical power from a source comprising:

- means for receiving power from said source,
- an oscillator coupled to said receiving means for transforming said received power to an output signal at a frequency and voltage for causing said lamp to produce visible light through gas discharge within said lamp envelope.

- a circuit board having said oscillator mounted thereon, and

- means attaching said circuit board for forming an end of said lamp envelope with said oscillator output signal connected to said lamp heater elements.

\* \* \* \* \*

Claims 7-25 added (see Preliminary Amendment).

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**COMBINED REISSUE DECLARATION, POWER OF ATTORNEY & PETITION**

**TYPE OF DECLARATION**

- ☐ Utility
- ☐ Design
- ☒ Reissue
- ☐ Supplemental
- ☐ Divisional
- ☐ Continuation
- ☐ Continuation-in-part
- ☐ National Stage of the PCT

**INVENTORSHIP AND SPECIFICATION IDENTIFICATION**

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one is listed below) or a joint inventor (if plural inventors are named below) of the invention entitled:

**GAS DISCHARGE LAMP AND POWER DISTRIBUTION SYSTEM THEREFOR**

as described and claimed in the reissue specification and Preliminary Amendment which

- ☒ is attached hereto.
- ☐ was filed on \_\_\_\_\_
  - ☐ as U.S. Serial Number.; or
  - ☐ Express Mail No. \_\_\_\_\_ (as serial number not yet known); and
  - ☐ was amended on \_\_\_\_\_.
- ☐ was described and claimed in PCT International Application No. PCT/ / filed on \_\_\_\_\_; and
  - ☐ as amended under PCT Article 19 and/or 34 on \_\_\_\_\_.

**REVIEW OF PAPERS AND DUTY OF CANDOR**

I have reviewed and understand the contents of the attached specification including the drawing and claims as amended by the Preliminary Amendment filed herewith; and

I acknowledge my duty to disclose information of which I am aware which is material to the examination of this application, in accordance with 37 CFR 1.56(a); and

- ☒ in compliance with this duty there is attached an information disclosure statement.

**PRIORITY CLAIMS**

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign applications for patent applications for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
(Number)	(Country)	(Day/Month/Year/Filed)	<input type="checkbox"/> Yes	<input type="checkbox"/> No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States Application(s) listed below and, insofar as the subject matter of each of the claims of this reissue application is not disclosed in the prior United States Application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this reissue application.

08/116,150	9/2/93	Issued U.S. Patent No. 5,485,057
(Application Serial No.)	(Filing Date)	(Status: Patented, Pending, Abandoned)

**Statements of Applicant Pursuant to 37 CFR 1.175**

This reissue application, enclosed herewith, is filed pursuant to 35 U.S.C. 251 in order to seek a broadening reissue of the claims of U.S. Patent No. 5,654,609. This reissue application is filed within two years of the grant of U.S. Patent No. 5,654,609.

Applicant believes the original patent (U.S. Patent No. 5,654,609) to be partly inoperative or invalid by reason of the patentee claiming less than patentee had the right to claim in the patent.

Applicant relies upon at least one error which supports this reissue application. For instance, the error which is relied upon by the Applicant to support this reissue application is the inclusion in original claim 1 of the language "a circuit board mounting said oscillator within a volume having a cross-section which is substantially the same as a cross-section of said lamp envelope", which is less than the patentee had the right to claim in the patent.

All errors which are being corrected in the present reissue application up to the time of filing of this declaration arose without any deceptive intention on the part of the Applicant.

**DECLARATION**

I declare that all statements made above of my own knowledge are true and all statements made on information and belief are believed to be true; and these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

**POWER OF ATTORNEY**

I hereby appoint the following patent attorneys and/or patent agent(s) with full power of appointment, substitution and revocation to prosecute this application, to make alterations and amendments thereto, to receive the patent, and to transact all business in the Patent Office connected therewith.

Joseph Sofer (Reg. No. 34,438)  
Robert M. Haroun (Reg. No. 34,345)  
Thomas C. Hughes (Reg. No. 42,674)

Please address all telephone calls and correspondence to:

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Suite 1921  
New York, New York 10173  
Telephone: (212) 697-2800  
Facsimile: (212) 697-3004

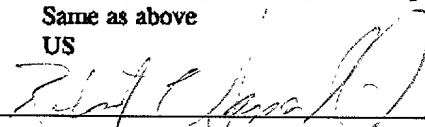
**PETITION**

Wherefore, I pray that Letters Patent be granted to me for the invention or discovery described and claimed in the above-mentioned specification and claims and accompanying Preliminary Amendment, and I hereby subscribe my name to the foregoing Declaration, Power of Attorney & Petition with references to the above-identified specification and claims.

**SIGNATURES**

Name of first joint inventor: Robert Smallwood

Home Address: 40081 Thomas Mill Road, Leesburg, VA 20175  
Post Office Address: Same as above  
Citizenship: US

Inventor's Signature: 

Date: 8-2-97

Name of second joint inventor: Michael Zarich

Home Address: 1614 Jetson Drive, Longmont, CO 80501  
Post Office Address: Same as above  
Citizenship: US

Inventor's Signature: \_\_\_\_\_

Date: \_\_\_\_\_

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
**COMBINED REISSUE DECLARATION, POWER OF ATTORNEY & PETITION**

**TYPE OF DECLARATION**

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- ☒ is attached hereto.  
☐ was filed on \_\_\_\_\_  
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☐ was described and claimed in PCT International Application No. PCT/ /  
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- ☒ in compliance with this duty there is attached an information disclosure statement.

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Prior Foreign Application(s)			Priority Claimed	
			<input type="checkbox"/>	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year/Filed)	Yes	No

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08/116,150	9/2/93	Issued U.S. Patent No. 5,485,057
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Applicant relies upon at least one error which supports this reissue application. For instance, the error which is relied upon by the Applicant to support this reissue application is the inclusion in original claim 1 of the language "a circuit board mounting said oscillator within a volume having a cross-section which is substantially the same as a cross-section of said lamp envelope", which is less than the patentee had the right to claim in the patent.

All errors which are being corrected in the present reissue application up to the time of filing of this declaration arose without any deceptive intention on the part of the Applicant.

### DECLARATION

I declare that all statements made above of my own knowledge are true and all statements made on information and belief are believed to be true; and these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

### POWER OF ATTORNEY

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Robert M. Haroun (Reg. No. 34,345)  
Thomas C. Hughes (Reg. No. 42,674)

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SOFER & HAROUN, L.L.P.  
342 Madison Avenue  
Suite 1921  
New York, New York 10173  
Telephone:(212)697-2800  
Facsimile:(212)697-3004

### PETITION

Wherefore, I pray that Letters Patent be granted to me for the invention or discovery described and claimed in the above-mentioned specification and claims and accompanying Preliminary Amendment, and I hereby subscribe my name to the foregoing Declaration, Power of Attorney & Petition with references to the above-identified specification and claims.

### SIGNATURES

Name of first joint inventor: Robert Smallwood

Home Address: 40081 Thomas Mill Road, Leesburg, VA 20175  
Post Office Address: Same as above  
Citizenship: US

Inventor's Signature: \_\_\_\_\_ Date: \_\_\_\_\_

Name of second joint inventor: Michael Zarich

Home Address: 1614 <sup>Judson</sup> ~~Jetson~~ Drive, Longmont, CO 80501  
Post Office Address: Same as above  
Citizenship: US

Inventor's Signature: Michael P. Zarich Date: 7/31/99



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

-----X  
In re Reissue Patent Application of  
Smallwood et al. :  
U.S. Patent No. 5,654,609 :  
Patent Granted: August 5, 1997 :  
Title: GAS DISCHARGE LAMP AND POWER :  
DISTRIBUTION SYSTEM THEREFOR :  
-----X

**REISSUE APPLICATION BY INVENTORS,  
OFFER TO SURRENDER PATENT UNDER 37 CFR 1.178**

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

S I R:

This Offer to Surrender Patent is submitted pursuant to 37 CFR 1.172, and is part of an application for a reissue patent based on U.S. Patent No. 5,654,609 filed herewith. We are the inventors of the original U.S. Patent No. 5,654,609. We hereby offer to surrender the original patent.

1. [X] Filed herewith is a certificate under 37 CFR 3.73(b).

2. [ ] Ownership of the patent is in the inventor(s), and no assignment of the patent has been made.

The written consent of all assignees owning an undivided interest in the original patent is

included in this application for reissue.

First joint inventor: Robert Smallwood  
Signature: \_\_\_\_\_  
Date: \_\_\_\_\_

First joint inventor: Michael Zarich  
Signature: Michael P. Zarich  
Date: 7/31/99

The Assignee owning an undivided interest in said original patent is LOGIC LABS, INC., and the Assignee consents to the accompanying application for reissue and offer to surrender patent.

I declare that all statements made above of my own knowledge are true and all statements made on information and belief are believed to be true; and these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Name of Assignee: LOGIC LABS, INC.  
Signature: \_\_\_\_\_  
Name: \_\_\_\_\_  
Title: \_\_\_\_\_  
Date: \_\_\_\_\_

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

-----X  
In re Reissue Patent Application of  
Smallwood et al. :  
U.S. Patent No. 5,654,609 :  
Patent Granted: August 5, 1997 :  
Title: GAS DISCHARGE LAMP AND POWER :  
DISTRIBUTION SYSTEM THEREFOR :  
-----X

REISSUE APPLICATION BY INVENTORS,  
OFFER TO SURRENDER PATENT UNDER 37 CFR 1.178

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

S I R:

This Offer to Surrender Patent is submitted pursuant to 37 CFR 1.172, and is part of an application for a reissue patent based on U.S. Patent No. 5,654,609 filed herewith. We are the inventors of the original U.S. Patent No. 5,654,609. We hereby offer to surrender the original patent.

1. ☒ Filed herewith is a certificate under 37 CFR 3.73(b).

2. ☐ Ownership of the patent is in the inventor(s), and no assignment of the patent has been made.

The written consent of all assignees owning an undivided interest in the original patent is

included in this application for reissue.

First joint inventor: Robert Smallwood  
Signature: *Robert Smallwood*  
Date: 8-2-99

First joint inventor: Michael Zarich  
Signature: \_\_\_\_\_  
Date: \_\_\_\_\_

The Assignee owning an undivided interest in said original patent is LOGIC LABS, INC., and the Assignee consents to the accompanying application for reissue and offer to surrender patent.

I declare that all statements made above of my own knowledge are true and all statements made on information and belief are believed to be true; and these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Name of Assignee: LOGIC LABS, INC.  
Signature: \_\_\_\_\_  
Name: \_\_\_\_\_  
Title: \_\_\_\_\_  
Date: \_\_\_\_\_

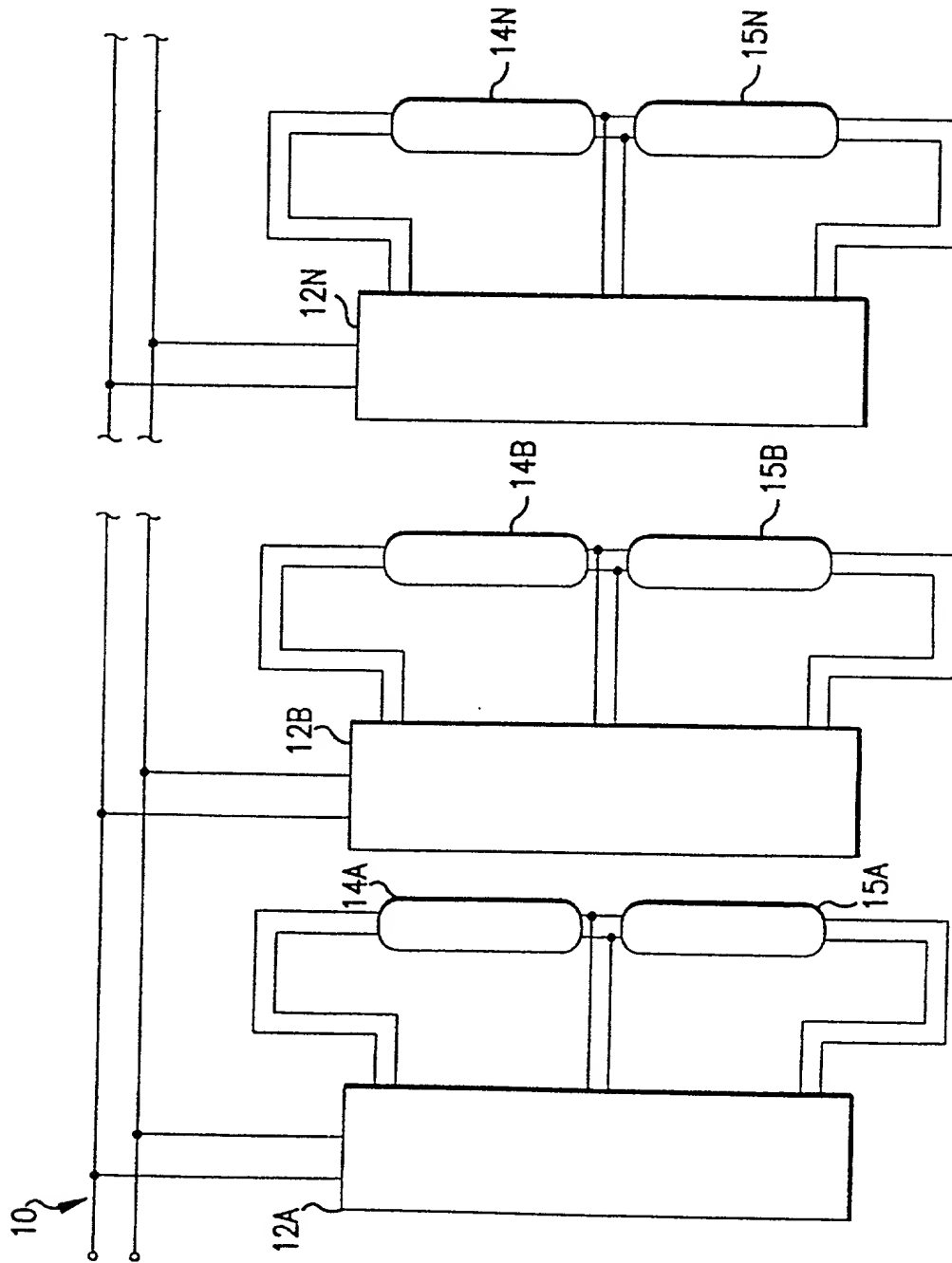


FIG. 1  
(PRIOR ART)

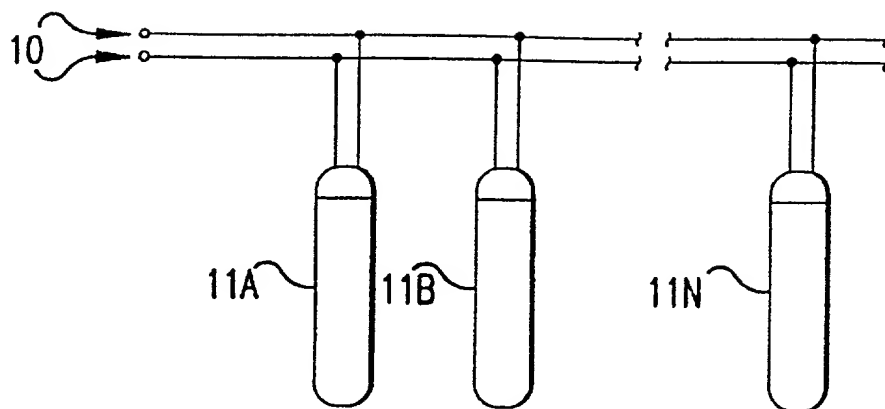


FIG. 2A

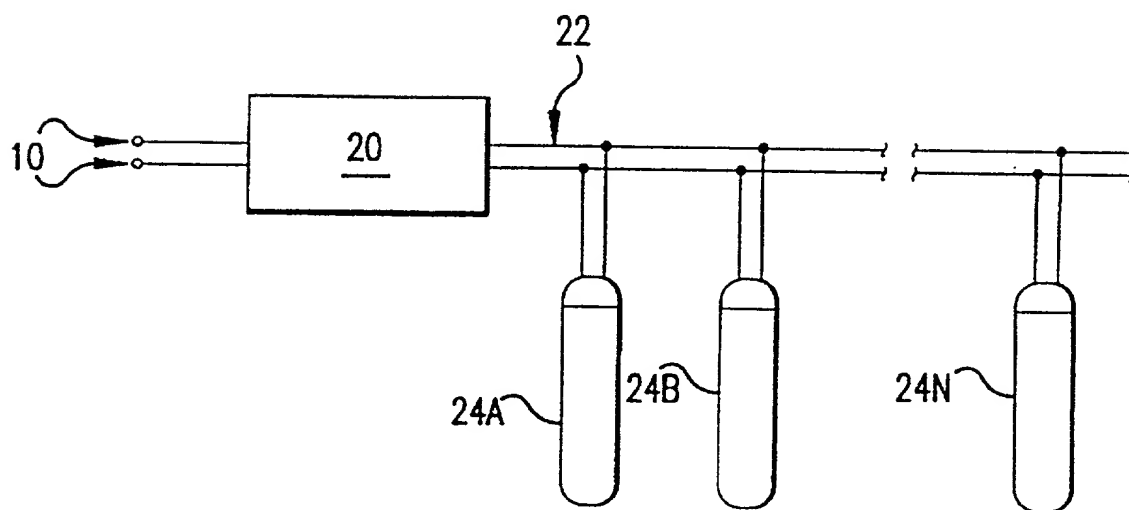


FIG. 2B

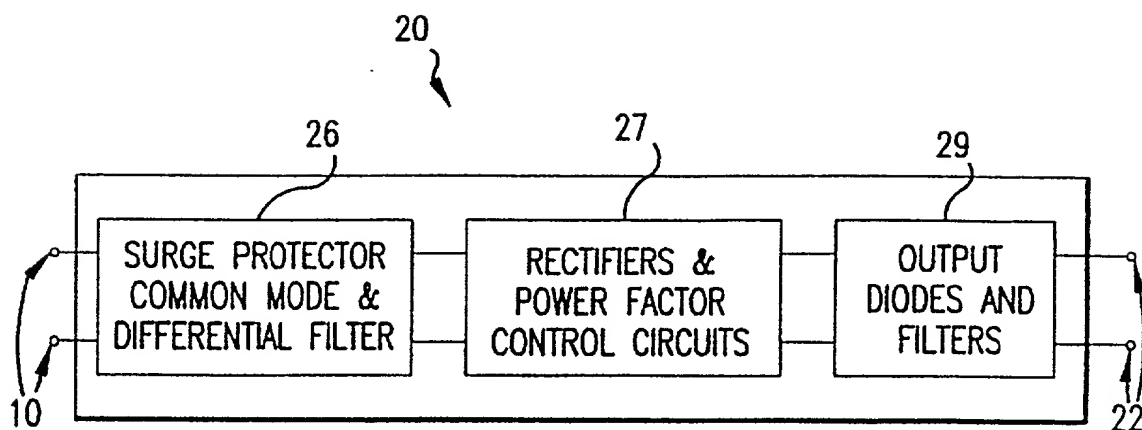


FIG.3

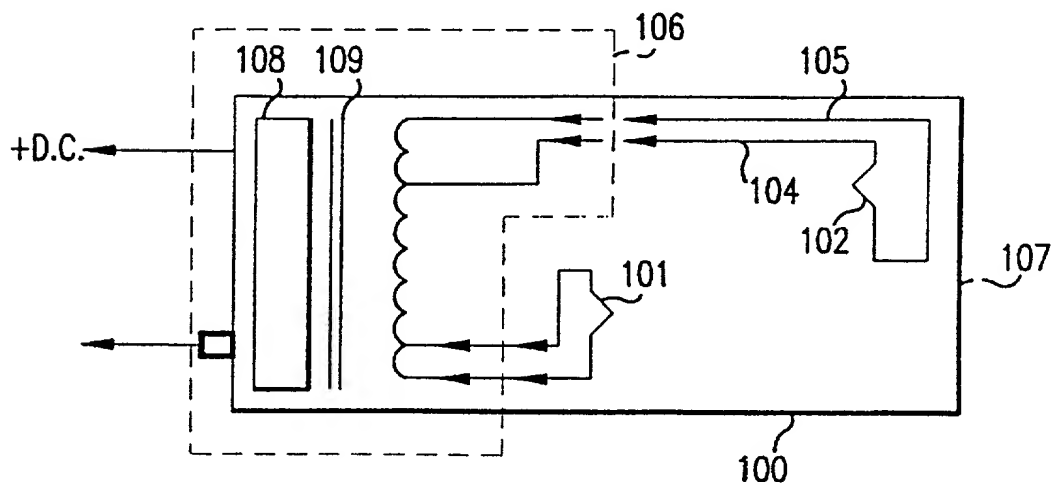


FIG.4

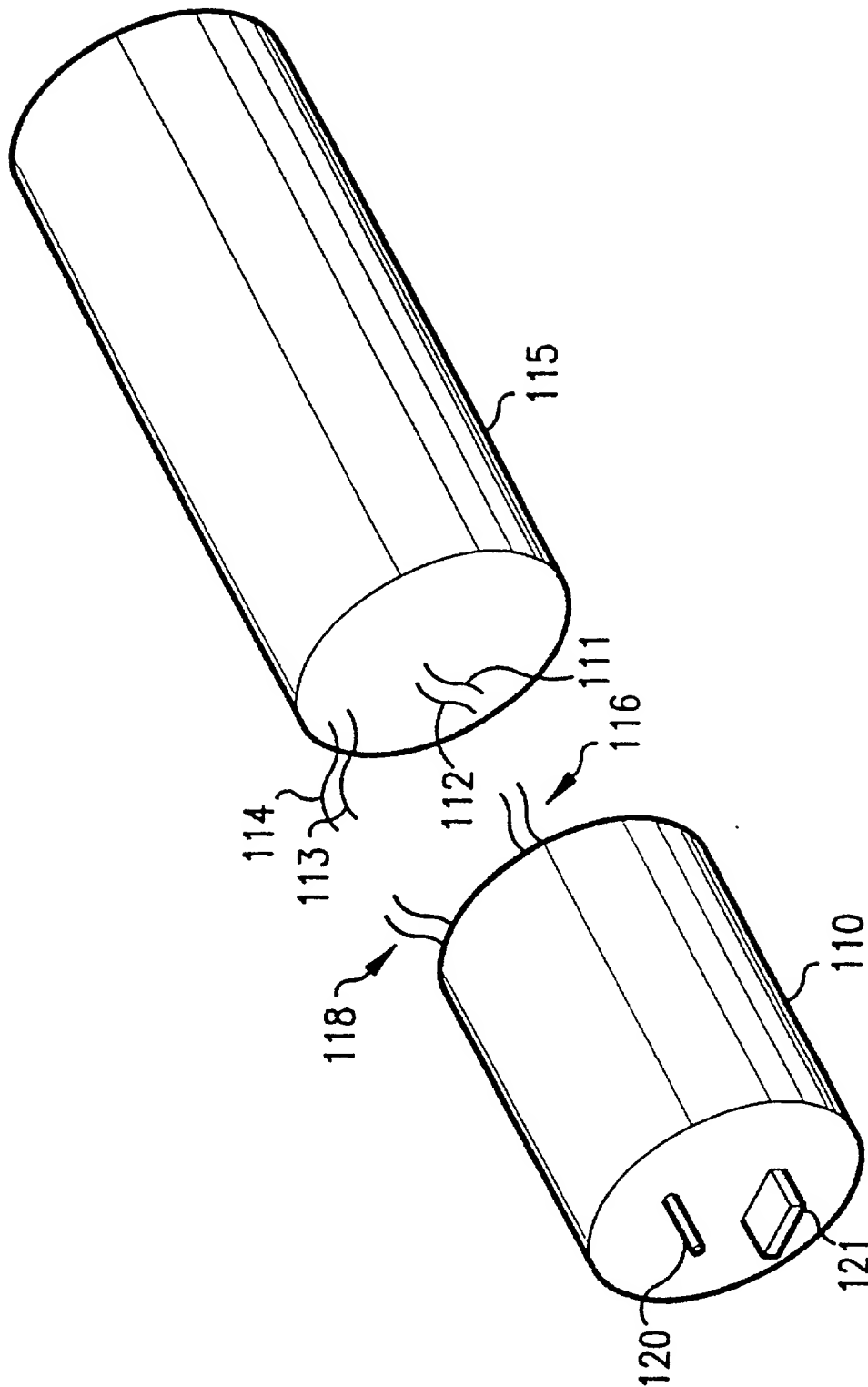


FIG. 5



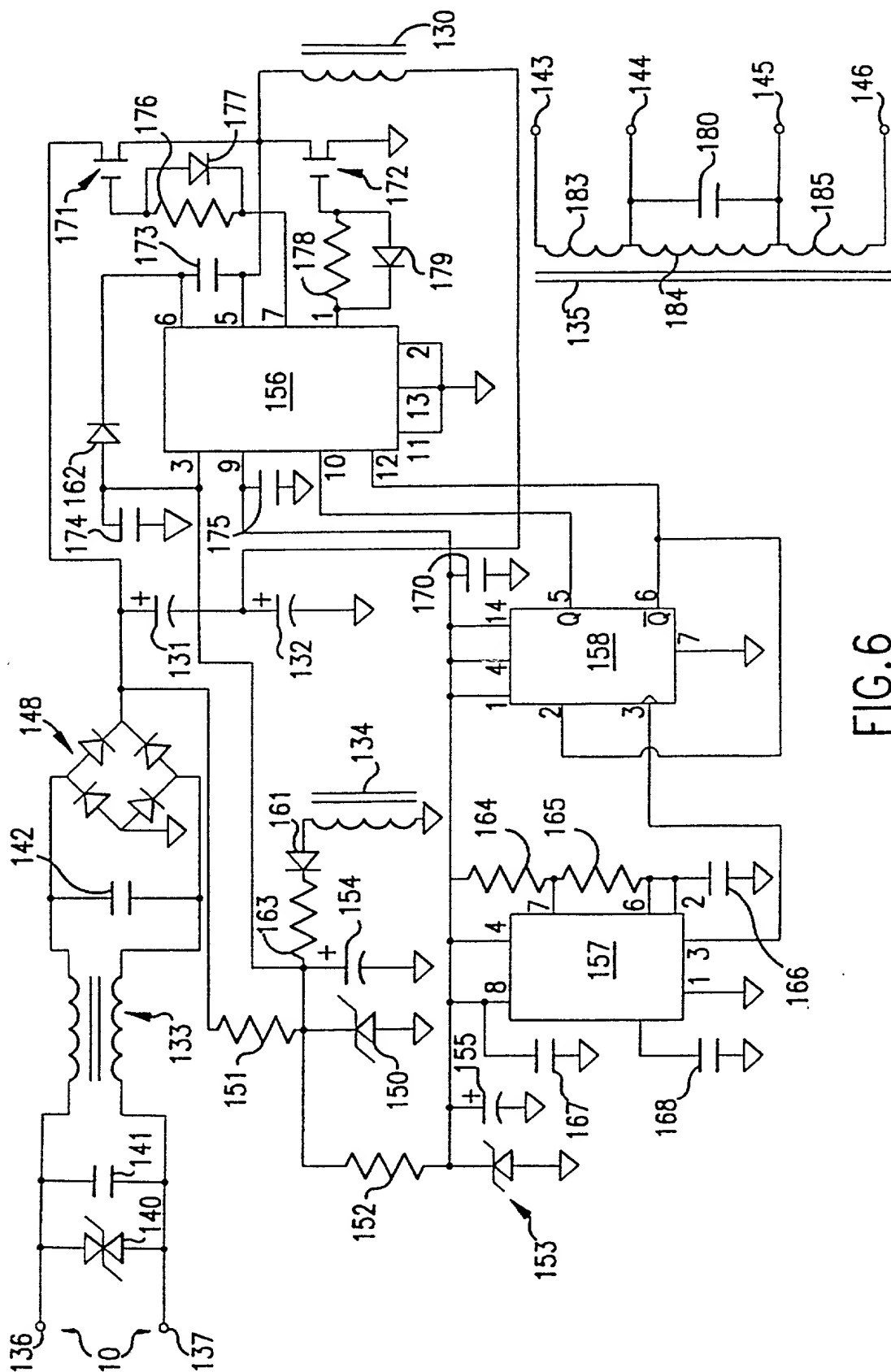


FIG. 6

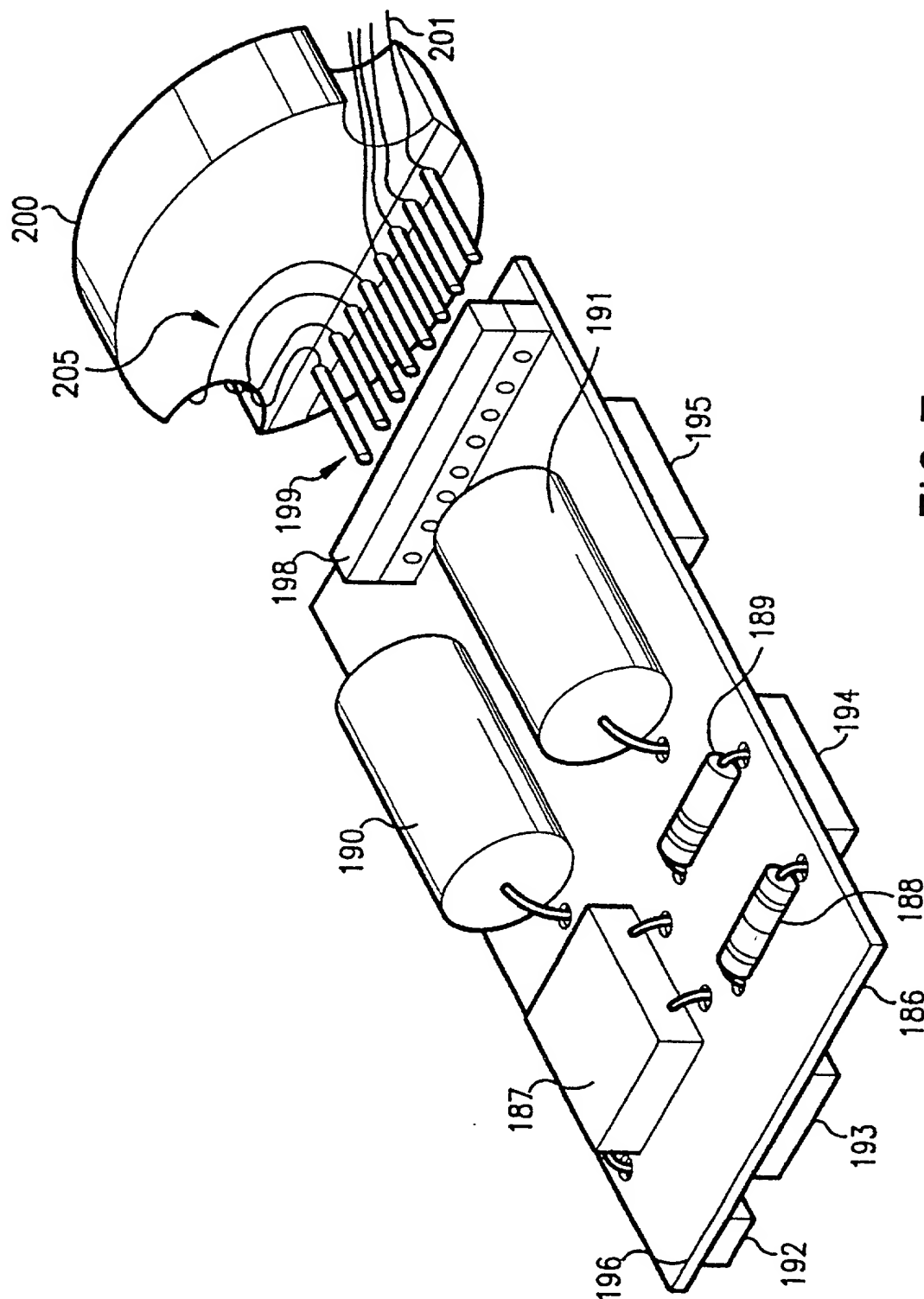


FIG. 7

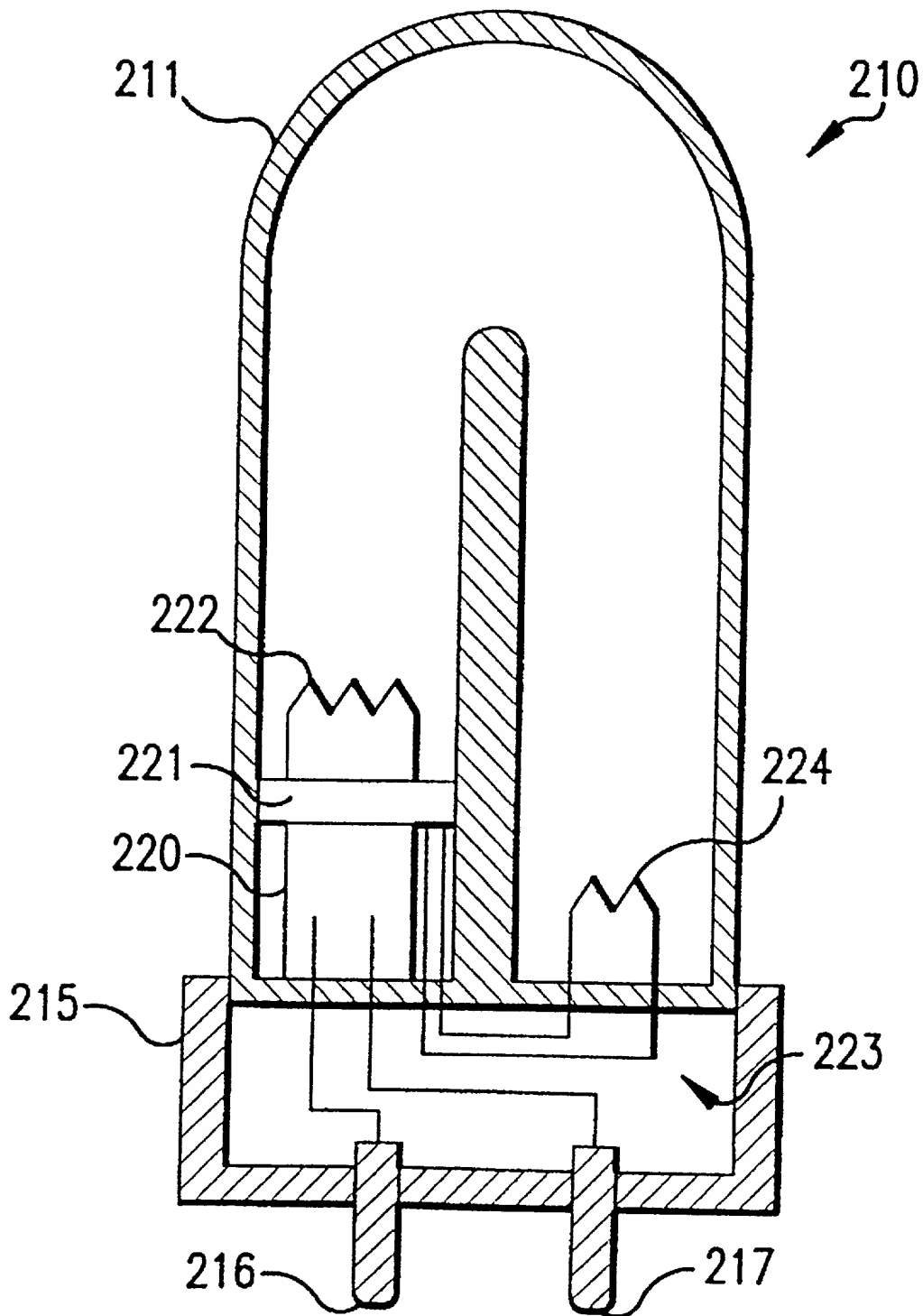


FIG.8